



Circuit is a design, some of my designs are not yet tested, use only for educational purpose. use as an idea for study only.
 put 104 CD cap for all ICs from positive to negative close to IC, even if omitted in circuit, for opamps on dual supply two caps.
 unused inputs of logic and opamps pull up or down to avoid oscillations and noise. connect supply of all chips if not mentioned.
 "analog ground" and "digital ground" must be linked at power supply only, avoid loops, let grounds radiate from a ground plane.
 use MFR 1% for all Resistors, 33E means 33 ohms, 22K means 22 kilo ohms, 1M is 1 megohm. 10T tp means ten turn trimpot.
 '474 CD' is 47 with 4 zeros pF, 470000 pF, 470 nF, 0.47uF, ceramic disc. "pl" is plastic, low leakage multilayer.

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Earlier This Circuit was 98SPCT41.pdf

delabs circuits and technologies		
Title 4-20mA Transmitter for RTD-PT100		
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